

TITLE OF THE INVENTION

SYSTEM FOR SIGNAL PROCESSING AND SIGNAL TRANSMISSION

BACKGROUND OF THE INVENTIONField of the Invention

5           This invention relates to a signal processing apparatus. In addition, this invention relates to a method of signal transmission. Furthermore, this invention relates to a signal transmission medium.

Description of the Related Art

10           Japanese published unexamined patent application 10-285234 discloses a signal processing circuit which discriminates a head packet and stores it into a memory in the reception of split packets via a serial interface. The signal processing circuit stores normal data into the memory even when a packet omission takes  
15           place. Split packets are transmitted via an IEEE1394 serial bus, a physical layer circuit, and a link core. Values DBC, FN, and DBS are set in a first quadlet of a CIP header of each split packet. The signal processing circuit discriminates a head packet by using values DBC, FN, and DBS therein. In addition, the signal processing circuit  
20           decides a packet omission by using values DBC, FN, and DBS. In the case where a packet omission is decided, the signal processing circuit stores only normal packets into a FIFO memory.

          Japanese published unexamined patent application 11-45512 discloses that packets inputted from an IEEE1394 serial bus are  
25           combined into a transport stream, and the transport stream is converted into a program stream before being recorded on a DVD (a

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The video file contains V packs (video packs).

In the case where an audio data stream having such a multiplexed file structure and containing multiple-channel audio information is transmitted in conformity with the IEEE1394  
5 standards, it is important to manage audio channels for accurate reproduction of the audio information. In a prior-art digital interface, it is difficult to easily capture channel information. Specifically, in a prior-art IEC958 system, it is difficult to perform channel management related to multiple-channel audio contents.

10 In the case of transmission of a data stream having a multiplexed file structure, it is desirable to implement a signal formatting process to previously notify a reception side of what decoding process is necessary. Specifically, the signal formatting process is designed to enable a digital interface to identify the type  
15 of compression by which compression-resultant data stored in the data stream are generated. Also, in this case, selective reception and efficient reproduction of the information from the data stream are desired. An example of the selective reception is as follows. When received data are of an undecodable type, a reception process  
20 is suspended. An example of the efficient reproduction is as follows. A previous decision is made as to whether or not data to be received is of a compressed type, and reproduction of information from the data is implemented in response to a result of the decision.

Generally, data of a DVD-Audio format represent high-quality  
25 audio information. In the case of transmission of high-quality audio data, it is important to consider copyright management. It is

sometimes desirable to permit making a copy in a manner such that high-quality audio data are converted into low-quality audio data (for example, audio data having a quality comparable to that of a CD) before the low-quality audio data are copied.

5                    SUMMARY OF THE INVENTION

It is a first object of this invention to provide an improved signal processing apparatus.

It is a second object of this invention to provide an improved method of signal transmission.

10           It is a third object of this invention to provide an improved signal transmission medium.

         A first aspect of this invention provides a signal processing apparatus comprising means for converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing PCM multiple-channel audio contents information; and means for enabling channel information and a portion of the audio contents information to be placed in adjacent portions of the given area respectively, the channel information corresponding to the portion of the audio contents information.

15           A second aspect of this invention provides a signal processing apparatus comprising means for receiving packets each having a given area assigned to real data, the packets resulting from conversion of a data stream containing audio packs storing PCM multiple-channel audio contents information, channel information and a portion of the audio contents information being placed in adjacent portions of the given area respectively, the channel

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information corresponding to the portion of the audio contents information; and means for decoding the channel information.

A third aspect of this invention provides a method of signal transmission. The method comprises the steps of converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing PCM multiple-channel audio contents information; enabling channel information and a portion of the audio contents information to be placed in adjacent portions of the given area respectively, the channel information corresponding to the portion of the audio contents information; and transmitting the packets via a serial interface.

A fourth aspect of this invention provides a signal transmission medium comprising means for converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing PCM multiple-channel audio contents information; means for enabling channel information and a portion of the audio contents information to be placed in adjacent portions of the given area respectively, the channel information corresponding to the portion of the audio contents information; and means for transmitting the packets from a transmission side to a reception side via a serial interface.

A fifth aspect of this invention provides a signal processing apparatus comprising means for converting a data stream containing an audio data stream into packets each having a given area assigned to real data, the audio data stream storing audio data resulting from a compression process; and means for enabling compression

information to be placed in the given area, the compressing information representing a type of the compression process.

A sixth aspect of this invention provides a signal processing apparatus comprising means for receiving packets each having a  
5 given area assigned to real data, the packets resulting from conversion of a data stream containing an audio data stream storing audio data resulting from a compression process, compression information being placed in the given area, the compression information representing a type of the compression process; and  
10 means for decoding the compression information.

A seventh aspect of this invention provides a method of signal transmission. The method comprises the steps of converting a data stream containing an audio data stream into packets each having a  
15 given area assigned to real data, the audio data stream storing audio data resulting from a compression process; enabling compression information to be placed in the given area, the compression information representing a type of the compression process; and transmitting the packets via a serial interface.

An eighth aspect of this invention provides a signal  
20 transmission medium comprising means for converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing audio data resulting from a compression process; means for enabling compression information to be placed in the given area, the compression  
25 information representing a type of the compression process; and means for transmitting the packets from a transmission side to a

reception side via a serial interface.

A ninth aspect of this invention provides a signal processing apparatus comprising means for converting a data stream containing audio packs into packets each having a given area; and means for  
5 enabling at least one of a down sampling flag, a down mix flag, and a dequantization flag to be placed in the given area.

A tenth aspect of this invention is based on the ninth aspect thereof, and provides a signal processing apparatus further comprising means for down-sampling and dequantizing main data  
10 into processing-resultant data, means for receiving a transmission request, and means for loading the packets with the processing-resultant data and transmitting the packets in response to the received transmission request.

An eleventh aspect of this invention provides a signal  
15 processing apparatus comprising means for receiving packets each having a given area, the packets resulting from conversion of a data stream containing audio packs, wherein at least one of a down sampling flag, a down mix flag, and a dequantization flag is placed in the given area; and means for decoding the at least one of the down  
20 sampling flag, the down mix flag, and the dequantization flag.

A twelfth aspect of this invention provides a method of signal transmission. The method comprises the steps of converting a data stream containing audio packs into packets each having a given area; enabling at least one of a down sampling flag, a down mix flag, and a  
25 dequantization flag to be placed in the given area; and transmitting the packets via a serial interface.

A thirteenth aspect of this invention is based on the twelfth aspect thereof, and provides a method further comprising the steps of down-sampling and dequantizing main data into processing-resultant data, receiving a transmission request, and loading the  
5 packets with the processing-resultant data and transmitting the packets in response to the received transmission request.

A fourteenth aspect of this invention provides a signal transmission medium comprising means for converting a data stream containing audio packs into packets each having a given area;  
10 means for enabling at least one of a down sampling flag, a down mix flag, and a dequantization flag to be placed in the given area; and means for transmitting the packets from a transmission side to a reception side via a serial interface.

A fifteenth aspect of this invention provides a signal  
15 processing apparatus comprising means for converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing audio data resulting from an encoding process; and means for enabling encoding information to be placed in the given area, the encoding information  
20 representing a type of the encoding process.

A sixteenth aspect of this invention provides a signal processing apparatus comprising means for receiving packets each having a given area assigned to real data, the packets resulting from conversion of a data stream containing audio packs storing audio  
25 data resulting from an encoding process, encoding information being placed in the given area, the encoding information



representing a type of the encoding process; and means for decoding the encoding information.

5 A seventeenth aspect of this invention provides a method of signal transmission. The method comprises the steps of converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing audio data resulting from an encoding process; enabling encoding information to be placed in the given area, the encoding information representing a type of the encoding process; and transmitting the  
10 packets via a serial interface.

An eighteenth aspect of this invention provides a signal transmission medium comprising means for converting a data stream containing audio packs into packets each having a given area assigned to real data, the audio packs storing audio data resulting  
15 from an encoding process; means for enabling encoding information to be placed in the given area, the encoding information representing a type of the encoding process; and means for transmitting the packets from a transmission side to a reception side via a serial interface.

20 A nineteenth aspect of this invention is based on the eighteenth aspect thereof, and provides a signal transmission medium wherein the encoding process comprises a 1-bit DSD encoding process.

25 A twentieth aspect of this invention is based on the fourteenth aspect thereof, and provides a signal transmission medium wherein the down sampling flag indicates halving an original sampling

frequency.

A twenty-first aspect of this invention is based on the eighth aspect thereof, and provides a signal transmission medium wherein the compression information comprises information representing  
5 that DSD encoded data are compressed by a predetermined compression process.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of a signal processing apparatus according to a first embodiment of this invention.

10 Fig. 2 is a flowchart of a segment of a control program for a controller within a disc player in Fig. 1.

Fig. 3 is a block diagram of a signal processing apparatus according to a second embodiment of this invention.

15 Fig. 4 is a flowchart of a segment of a control program for a controller within a disc player in Fig. 3.

Fig. 5 is a flowchart a segment of a control program for a controller within a reception side in a third embodiment of this invention.

Fig. 6 is a flowchart of a block in Fig. 5.

20 Fig. 7 is a diagram of a data structure of a linear PCM audio pack.

Fig. 8 is a diagram of a data structure of a packed PCM audio pack.

Fig. 9 is a diagram of a data structure of a video pack.

25 Fig. 10 is a diagram of a data structure of a real time information pack.

Fig. 11 is a diagram of a data structure of a still picture pack.

Fig. 12 is a time-domain diagram of signals which occur during signal conversion for isochronous transmission conforming to the IEEE1394 standards.

5        Fig. 13 is a diagram of a cycle start packet, an isochronous packet, and a data structure of the isochronous packet.

Fig. 14 is a diagram of the details of management information in a real data tail in Fig. 13.

10       Fig. 15 is a diagram of a format of 32-bit multiple-channel audio data in real data in Fig. 13.

Fig. 16 is a diagram of the channel assignment of words of a channel code in Fig. 15.

15       Fig. 17 is a diagram of an example of the real data in Fig. 13 which have a set of 32-bit segments each corresponding to 32-bit multiple-channel audio data in Fig. 15.

Fig. 18 is a diagram of a structure of 16-bit information containing channel information in a fourth embodiment of this invention.

Fig. 19 is a diagram of channel assignment.

20       Fig. 20 is a diagram of a format of 32-bit multiple-channel audio data which is used instead of the 32-bit multiple-channel audio data format in Fig. 15.

Fig. 21 is a block diagram of a disc player according to a fifth embodiment of this invention.

25       Fig. 22 is a block diagram of a recording and reproducing device according to a sixth embodiment of this invention.

Fig. 23 is a flowchart of a segment of a control program for an audio pack processor in Fig. 22.

Fig. 24 is a flowchart of a program segment which can replace the program segment in Fig. 23.

5        Fig. 25 is a flowchart of a segment of a control program for a control unit within a disc player in a seventh embodiment of this invention.

10       Fig. 26 is a flowchart of a segment of a control program for a controller within a recording and reproducing device in the seventh embodiment of this invention.

Fig. 27 is a diagram of a format of 32-bit audio data in an eighth embodiment of this invention which is used instead of the 32-bit audio data format in Fig. 15 or Fig. 20.

15       Fig. 28 is a flowchart of a program block which substitutes for the program block in Fig. 6.

Fig. 29 is a block diagram of a disc player in the eighth embodiment of this invention.

Fig. 30 is a block diagram of a recording and reproducing device in the eighth embodiment of this invention.

20       Fig. 31 is a flowchart of a segment of a control program for a PCM-DSD converter in Fig. 30.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

### First Embodiment

25       Fig. 1 shows a signal processing apparatus according to a first embodiment of this invention. The signal processing apparatus of Fig. 1 includes a disc player 100 and a recording and reproducing

device 200. The disc player 100 operates as a transmitter being a center of a domestic information network. The recording and reproducing device 200 operates as a reception terminal.

5 The disc player 100 is connected to a data transmission interface 200a. It should be noted that the data transmission interface 200a may be provided in the disc player 100. The recording and reproducing device 200 is connected to a data transmission interface 200b. It should be noted that the data transmission interface 200b may be provided in the recording and  
10 reproducing device 200. The data transmission interfaces 200a and 200b are connected to each other via two IEEE1394 serial interfaces 188-1 and 188-2 including IEEE1394 serial buses.

The disc player 100 reads out an audio signal, a still picture signal, and a real time information signal from a DVD-Audio. The  
15 disc player 100 may also read out a video signal from the DVD-Audio. The disc player 100 transmits the audio signal, the still picture signal, and the real time information signal to the recording and reproducing device 200 via the data transmission interface 200a, the serial interfaces 188-1 and 188-2, and the data transmission  
20 interface 200b. Also, the disc player 100 may transmit the video signal. The disc player 100 and the recording and reproducing device 200 communicate with each other via the data transmission interface 200a, the serial interfaces 188-1 and 188-2, and the data transmission interface 200b.

25 It is preferable that as viewed from the disc player 100, one of the serial interfaces 188-1 and 188-2 is used exclusively for signal

transmission to the recording and reproducing device 200, and the other serial interface is used selectively for signal transmission to the recording and reproducing device 200 and signal reception therefrom.

- 5           The disc player 100 includes a controller having a microcomputer, a CPU, or a similar device. The controller operates in accordance with a control program stored in its internal ROM. The controller can access the data transmission interface 200a. The controller can access the serial interfaces 188-1 and 188-2 via  
10   the data transmission interface 200a. The controller may directly access the serial interfaces 188-1 and 188-2. The controller is able to control the data transmission interface 200a and the serial interfaces 188-1 and 188-2. Different identification (ID) numbers are assigned to the serial interfaces 188-1 and 188-2, respectively.  
15   The controller can discriminate the serial interfaces 188-1 and 188-2 from each other by referring to the ID numbers thereof.

          The disc player 100 can be connected to an external loudspeaker and an external display via an output terminal 55 for monitor. The audio signal can be transmitted from the disc player  
20   100 to the loudspeaker via the output terminal 55, being converted into corresponding sounds by the loudspeaker. Each of the video signal, the still picture signal, and the real time information signal can be transmitted from the disc player 100 to the display via the output terminal 55, being visualized by the display.

- 25           Fig. 2 is a flowchart of a segment of the control program for the controller within the disc player 100. With reference to Fig. 2,

a first step S1 of the program segment sets the serial interface 188-1 in a receiving operation mode as viewed from the disc player 100. Furthermore, the step S1 sets the serial interface 188-2 in a transmitting operation mode as viewed from the disc player 100.

5       A step S2 following the step S1 implements two-way communications with the data transmission interface 200b (the recording and reproducing device 200) via the data transmission interface 200a and the serial interfaces 188-1 and 188-2. During the two-way communication, operation command signals such as a  
10       play command signal and a disc request signal are transmitted.

      A step S3 subsequent to the step S2 changes the serial interface 188-1 to a transmitting operation mode as viewed from the disc player 100.

      A step S4 following the step S3 transmits plural signals to the  
15       data transmission interface 200b (the recording and reproducing device 200) via the data transmission interface 200a and the serial interfaces 188-1 and 188-2. The plural signals include high-transfer-rate signals reproduced from a DVD-Audio. Examples of the high-transfer-rate signals are an audio signal, a real time  
20       information signal, and a still picture signal. The step S4 assigns the transmission of at least first one of the high-transfer-rate signals to the serial interface 188-1. The step S4 assigns the transmission of at least second one of the high-transfer-rate signals to the serial interface 188-2. For example, the transmission of an audio signal is  
25       assigned to the serial interface 188-1 while the transmission of a real time information signal and a still picture signal is assigned to

the serial interface 188-2. In the case where the audio signal, the real time information signal, and the still picture signal are synchronously reproduced, the distributed signal transmission removes a significant limitation on a buffer capacity so that a lot of  
5 still pictures (for example, 80 to 99 still pictures) can be reproduced synchronously with audio information.

A step S5 subsequent to the step S4 decides whether or not the signal transmission has been completed. When the signal transmission has been completed, the program exits from the step  
10 S5 and then the current execution cycle of the program segment ends. When the signal transmission has not been completed yet, the program returns from the step S5 to the step S1.

It should be noted that the IEEE1394 transmission system may be replaced by a transmission system using an audio-related  
15 format conforming to the IEC958 standards. The audio-related format conforming to the IEC958 standards may be an IEC958 mode audio-related format prescribed in the IEEE standards.

#### Second Embodiment

Fig. 3 shows a signal processing apparatus according to a  
20 second embodiment of this invention. The signal processing apparatus of Fig. 3 is similar to the signal processing apparatus of Fig. 1 except for design changes indicated hereinafter.

The signal processing apparatus of Fig. 3 includes a disc player 100A instead of the disc player 100 (see Fig. 1). The signal  
25 processing apparatus of Fig. 3 includes data transmission interfaces 200aa and 200bb replacing the data transmission interfaces 200a



and 200b respectively. The data transmission interfaces 200aa and 200bb are connected to each other via four IEEE1394 serial interfaces 188-1, 188-2, 188-3, and 188-4 including IEEE1394 serial buses.

5           Fig. 4 is a flowchart of a segment of a control program for a controller within the disc player 100A. With reference to Fig. 4, a first step S11 of the program segment sets the serial interface 188-1 in a receiving operation mode as viewed from the disc player 100A. Furthermore, the step S11 sets the serial interfaces 188-2, 10 188-3, and 188-3 in a transmitting operation mode as viewed from the disc player 100A.

A step S12 following the step S11 implements two-way communications with the data transmission interface 200bb (the recording and reproducing device 200) via the data transmission 15 interface 200a and the serial interfaces 188-1 and 188-2.

A step S13 subsequent to the step S12 changes the serial interface 188-1 to a two-way communication mode.

A step S14 following the step S13 transmits plural signals to the data transmission interface 200bb (the recording and 20 reproducing device 200) via the data transmission interface 200aa and the serial interfaces 188-2, 188-3, and 188-4. The plural signals include high-transfer-rate signals reproduced from a DVD-Audio. Examples of the high-transfer-rate signals are an audio signal, a real time information signal, and a still picture signal. The 25 step S14 assigns the transmission of at least first one of the high-transfer-rate signals to the serial interface 188-2. The step S14

assigns the transmission of at least second one of the high-transfer-rate signals to the serial interface 188-3. The step S14 assigns the transmission of at least third one of the high-transfer-rate signals to the serial interface 188-4. For example, the transmission of an  
5 audio signal is assigned to the serial interface 188-2, and the transmission of a real time information signal and the transmission of a still picture signal are assigned to the serial interfaces 188-3 and 188-4 respectively.

In addition, the step S14 implements two-way  
10 communications with the data transmission interface 200bb (the recording and reproducing device 200) via the data transmission interface 200a and the serial interface 188-1. During the two-way communications, operation-related data are transmitted.

A step S15 subsequent to the step S14 decides whether or  
15 not the signal transmission has been completed. When the signal transmission has been completed, the program exits from the step S15 and then the current execution cycle of the program segment ends. When the signal transmission has not been completed yet, the program returns from the step S15 to the step S11.

20 Third Embodiment

A signal processing apparatus of a third embodiment of this invention is similar to the signal processing apparatus of Fig. 1 or Fig. 3 except for additional designs indicated hereinafter.

A transmission side (the disc player 100 or 100A) includes a  
25 microcomputer-based controller or a CPU-based controller which controls operation of the transmission side. Similarly, a reception

side (the recording and reproducing device 200) includes a microcomputer-based controller or a CPU-based controller which controls operation of the reception side.

Main data (contents information) to be sent from the

5 transmission side to the reception side result from compression of original data. The transmission side generates a flag representing whether or not the data compression is of the DVD-Audio lossless type. The compression type flag is placed in management information within a data field. The transmission side sends the

10 management information inclusive of the compression type flag to the reception side. In the case where the reception side is incapable of decoding lossless-compression-resultant data, the reception side suspends data reception when the received compression type flag represents that the data compression is of

15 the DVD-Audio lossless type. The transmission side sends authentication data to the reception side. The reception side generates an authentication response signal when receiving the authentication data. The reception side sends the authentication response signal to the transmission side. The transmission side

20 decides whether or not the reception side is authorized to make a copy of the main data on the basis of the authentication response signal. When the reception side is authorized to make a copy of the main data, the transmission side starts the transmission of the main data.

25 The transmission side sends various flags and copy-related information to the reception side in addition to the main data. The

various flags include an account flag, a zero flag, a mute flag, a pack  
flag (a pack type flag or a signal type flag), and a copy flag. The  
account flag depends on the type of contents information, and  
represents whether the contents information corresponds to "free"  
5 or "paid". The copy-related information includes a down sampling  
flag Fa, a down mix flag Fb, a dequantization flag Fc, and an  
information piece representing the number of times of copying (the  
copy generation number). The down mix flag Fb is set to "1" when a  
process of halving a sampling frequency Fs is implemented. The  
10 dequantization flag Fc is set to "1" when a process of dequantization  
from 20 bits to 16 bits is implemented. The number of times of  
copying which is represented by the piece of the copy-related  
information is either set to a given value or updated to a new value.

In the case where one or more of the serial interfaces 188-1,  
15 188-2, 188-3, and 188-4 are not used, the transmission side sends  
zero flags of "1" to the reception side via the non-used serial  
interfaces. In the case where the main data continue to be "0", the  
transmission side sends a zero flag of "1" to the reception side. In  
the case of transmission of a still picture signal or a real time  
20 information signal via one of the serial interfaces 188-1, 188-2,  
188-3, and 188-4, the transmission side sends a mute flag of "1" to  
the reception side via the present serial interface. In the case of  
transmission of an audio signal, a still picture signal, a real time  
information signal, or a video signal via one of the serial interfaces  
25 188-1, 188-2, 188-3, and 188-4, the transmission side sends a  
signal type flag (a pack flag) representative of the type of the

transmitted signal to the reception side via the present serial interface.

Fig. 5 is a flowchart of a segment of a control program for the controller within the reception side (the recording and reproducing device 200). With reference to Fig. 5, a first step S21 of the program segment detects received flags and received copy-related information. The step S21 looks into an account flag among the detected flags. When the account flag represents that contents information corresponds to "paid", the step S21 refers to a received copy-related information piece representative of the number of times of copying and determines a charge in response to the number of times of copying. Then, the step S21 executes account management including a process of electronically paying the charge from an electronic purse provided in the reception side.

A step S22 following the step S21 decides whether or not a zero flag of "1" is received. When a zero flag of "1" is received, the program advances from the step S22 to a step S23. Otherwise, the program advances from the step S22 to a step S24.

The step S23 prevents a main-data receiving process from being executed. After the step S23, the program advances to a step S27.

The step S24 decides whether or not a mute flag of "1" (or mute flags of "1") is received. When a mute flag of "1" is received, the program advances from the step S24 to a step S25. Otherwise, the program advances from the step S24 to a block S26.

The step S25 executes a muting process on a signal received

via the serial interface related to the mute flag of "1". After the step S25, the program advances to the block S26.

The block S26 looks into a signal type flag (a pack flag) among the received flags, and executes the main-data receiving process in response to the signal type flag. After the block S26, the program advances to the step S27.

The step S27 decides whether or not the reception of the main data has been completed. When the reception of the main data has been completed, the program exits from the step S27 and then the current execution cycle of the program segment ends. When the reception of the main data has not been completed yet, the program returns from the step S27 to the step S21.

The reception side notifies the transmission side of the completion of the reception of the main data, that is, the completion of copying the main data. The transmission side updates or counts up the number of times of copying, which is represented by the piece of the copy-related information, in response to the notice from the reception side.

Fig. 6 shows the details of the block S26 in Fig. 5. With reference to Fig. 6, a first step S31 in the block S26 decides whether or not the signal type flag corresponds to an audio signal (a DVD-Audio A pack, that is, a DVD-Audio audio pack). When the signal type flag corresponds to an audio signal, the program advances from the step S31 to a step S32. Otherwise, the program advances from the step S31 to a step S33.

The step S32 feeds a received signal (the received main data)

to a buffer assigned to audio packs and provided in the reception side. After the step S32, the program jumps to the step S27 (see Fig. 5).

5 The step S33 decides whether or not the signal type flag corresponds to a video signal (a DVD-Audio V pack, that is, a DVD-Audio video pack). When the signal type flag corresponds to a video signal, the program advances from the step S33 to a step S34. Otherwise, the program advances from the step S33 to a step S35.

10 The step S34 feeds a received signal (the received main data) to a buffer assigned to video packs and provided in the reception side. After the step S34, the program jumps to the step S27 (see Fig. 5).

15 The step S35 decides whether or not the signal type flag corresponds to a real time information signal (a DVD-Audio RTI pack, that is, a DVD-Audio real time information pack). When the signal type flag corresponds to a real time information signal, the program advances from the step S35 to a step S36. Otherwise, the program advances from the step S35 to a step S37.

20 The step S36 feeds a received signal (the received main data) to a buffer assigned to real time information packs and provided in the reception side. After the step S36, the program jumps to the step S27 (see Fig. 5).

25 The step S37 decides whether or not the signal type flag corresponds to a still picture signal (a DVD-Audio SPCT pack, that is, a DVD-Audio still picture pack). When the signal type flag corresponds to a still picture signal, the program advances from the

step S37 to a step S38. Otherwise, the program advances from the step S37 to a step S39.

The step S38 feeds a received signal (the received main data) to a buffer assigned to still picture packs and provided in the reception side. After the step S38, the program jumps to the step S27 (see Fig. 5).

The step S39 is executed when the signal type flag corresponds to other signals, for example, management data or management information. The step S39 feeds a received signal (the received main data) to a decoder buffer provided in the reception side. After the step S39, the program advances to the step S27 (see Fig. 5).

The above-mentioned management information and the above-mentioned flags are transmitted while being placed in data according to the MPEG protocol as will be described below.

An MPEG transport stream generated in the transmission side (the disc player 100 or 100A) includes a bit stream of audio-based data conforming to the DVD-Audio standards. The MPEG transport stream may include a bit stream of video data and audio data conforming to the DVD-Video standards or a bit stream of audio data conforming to the SACD (super audio CD) standards. Specifically, the MPEG transport stream has a sequence of 188-byte packets loaded with audio packs (A packs), video packs (V packs), real time information packs (RTI packs), and still picture packs (SPCT packs). The audio packs are of a linear PCM type and a packed PCM type (a compression PCM type).



Linear PCM audio packs are recorded on a data area of a DVD-Audio. As shown in Fig. 7, one linear PCM audio pack has a length of 2,048 bytes or less, and is composed of a 14-byte pack header and an audio packet. The pack header is followed by the audio packet.

- 5 The audio packet has a sequence of a packet header, a private header, and audio data (linear PCM audio data). The packet header has 9 bytes, 14 bytes, or 17 bytes. The audio data has 1 byte to 2,011 bytes.

- 10 The private header in the linear PCM audio pack has a sequence of 8-bit sub stream ID (identification) information, a 3-bit reserved area, 5-bit information of an UPC/EAN-ISRC (Universal Product Code/European Article Number-International Standard Recording Code) number, 8-bit information of UPC/EAN-ISRC data, 8-bit information of the private header length, a 16-bit first access  
15 unit pointer, 8-byte audio data information ADI, and 0 to 7 stuffing bytes.

- The audio data information ADI in the linear PCM audio pack has a sequence of a 1-bit audio emphasis flag, a 1-bit reserved area, 1-bit information of a stereophonic playback mode, 1-bit  
20 information of down mix code effectiveness, a 4-bit down mix code, 4-bit information of the quantization word length (the quantization bit number) in a channel group "1", 4-bit information of the quantization word length (the quantization bit number) in a channel group "2", 4-bit information of the audio sampling frequency fs1 in  
25 the channel group "1", 4-bit information of the audio sampling frequency fs2 in the channel group "2", a 4-bit reserved area, 4-bit

information of a multiple channel type, 3-bit information of a bit shift in the channel group "2", 5-bit channel assignment information, 8-bit dynamic-range control information, and a 16-bit reserved area. The channel assignment information is also referred to as the channel information.

Packed PCM audio packs (compression PCM audio packs) are recorded on a data area of a DVD-Audio. As shown in Fig. 8, one packed PCM audio pack (one compression PCM pack) has a length of 2,048 bytes or less, and is composed of a 14-byte pack header and an audio packet. The pack header is followed by the audio packet. The audio packet has a sequence of a packet header, a private header, and audio data (packed PCM audio data). The packed PCM audio data result from a lossless compression process. The packet header has 9 bytes, 14 bytes, 17 bytes, 19 bytes, or 22 bytes. The audio data has 1 byte to 2,015 bytes.

The private header in the packed PCM audio pack has a sequence of 8-bit sub stream ID (identification) information, a 3-bit reserved area, 5-bit information of an UPC/EAN-ISRC (Universal Product Code/European Article Number-International Standard Recording Code) number, 8-bit information of UPC/EAN-ISRC data, 8-bit information of the private header length, a 16-bit first access unit pointer, 4-byte audio data information ADI, and 0 to 7 stuffing bytes.

The audio data information ADI in the packed PCM audio pack contains an 8-bit forward search pointer, an 8-bit backward search pointer, and a 16-bit reserved area.

Video packs are recorded on a data area of a DVD-Audio or a DVD-Video. As shown in Fig. 9, one video pack has a length of 2,048 bytes or less, and is composed of a 14-byte pack header and a user data packet. The pack header is followed by the user data  
5 packet. The pack header has a sequence of 4-byte pack start information, 6-byte SCR (system clock reference) information, 3-byte MUX (multiplexed) rate information, and 1-byte stuffing data. The user data packet is loaded with 2,034-byte user data.

Real time information packs are recorded on a data area of a  
10 DVD-Audio. As shown in Fig. 10, one real time information pack has a length of 2,048 bytes or less, and is composed of a 14-byte pack header and a real time information packet. The pack header is followed by the real time information packet. The real time information packet has a sequence of a packet header, a private  
15 header, and real time information data. The packet header has 9 bytes, 14 bytes, or 17 bytes. The real time information data has 1 byte to 2,015 bytes.

The private header of the real time information packet has a sequence of 1-byte sub stream ID (identification) information, a 2-  
20 byte reserved area, 1-byte information of the private header length, 1-byte real time information identification (ID) data, and 0 to 7 stuffing bytes. The real time information ID data are divided into a 4-bit reserved area and a 4-bit effective data area.

Still picture packs are recorded on a data area of a DVD-Audio.  
25 As shown in Fig. 11, one still picture pack has a length of 2,048 bytes or less, and is composed of a 14-byte pack header and a still

picture packet. The pack header is followed by the still picture packet. The still picture packet has a sequence of a packet header and still picture data. The packet header has 9 bytes, 19 bytes, or 22 bytes. The still picture data has 1 byte to 2,025 bytes.

5           For example, each of the serial interfaces 188-1, 188-2, 188-3, and 188-4 (see Figs. 1 and 3) has the function of generating isochronous packets from data outputted by the data transmission interface 200a or 200aa (see Figs. 1 and 3). Each of the serial interfaces 188-1, 188-2, 188-3, and 188-4 transmits the generated  
10 isochronous packets. Operation of the serial interfaces 188-1, 188-2, 188-3, and 188-4 will be described in more detail below.

          With reference to Fig. 12, an MPEG transport stream generated in the transmission side (the disc player 100 or 100A) has a sequence of 188-byte packets. Time stamps referred to as  
15 source packet headers and each having 4 bytes are added to the front ends of 188-byte packets to form 192-byte signal segments respectively. In the reception side, voices and moving pictures are sequentially played back in accordance with the time stamps.

          Each 192-byte signal segment is divided into four 48-byte data  
20 blocks. One 192-byte signal segment may be used as one 192-byte data block. One 192-byte signal segment may be divided into two 96-byte data blocks. One 192-byte signal segment may be divided into eight 24-byte data blocks.

          The data blocks are arranged into groups each having two or  
25 more successive data blocks. In each group, data blocks are connected or combined. Packet headers for IEEE1394 are added to

the front ends of the groups to form isochronous packets respectively. A time interval of 125  $\mu$ s is defined as a 1-cycle period. At most one isochronous packet is placed in a 1-cycle period.

- 5           With reference to Fig. 13, arbitration is added to the head of each of cycle start packets and isochronous packets. During transmission, arbitration-added cycle start packets are placed in 1-cycle periods respectively. Normally, in a 1-cycle period, an arbitration-added cycle start packet is followed by an arbitration-
- 10       added isochronous packet or a plurality of arbitration-added isochronous packets. An arbitration-added isochronous packet may be absent from a 1-cycle period.

- One isochronous packet has a sequence of a packet header, a data field, and a 32-bit data error detection code. The packet
- 15       header has a sequence of 16-bit data length information, a 2-bit tag, 6-bit packet transmission channel assignment information, a 4-bit transaction code, a 4-bit sync code, and a 32-bit packet header error detection code. The tag represents whether a CIP (common isochronous packet) header is present or absent. The transaction
- 20       code indicates a processing code.

- The data field in the isochronous packet has a sequence of a 32-bit CIP header, a 32-bit real data header, real data, and a 32-bit real data tail. The real data header contains 8-bit ID (identification) information, 16-bit application information, and 8-bit copy-related
- 25       information. The ID information in the real data header has a 4-bit piece representing the total number of serial interfaces (the serial

interfaces 188-1, 188-2, 188-3, and 188-4), and a 4-bit piece of the ID number of the serial interface via which the present isochronous packet is transmitted. The application information in the real data header has a 4-bit pack flag (a 4-bit signal type flag), a 1-bit zero  
5 flag, a 1-bit mute flag, an 8-bit account flag, and a 2-bit copy flag. The pack flag represents the type of a related pack. Specifically, the pack flag represents which of an audio pack, a video pack, a real time information pack, and a still picture pack the related pack is equal to. The copy-related information in the real data header has a  
10 1-bit down sampling flag Fa, a 1-bit down mix flag Fb, a 1-bit dequantization flag Fc, and a 5-bit piece representing the number of times of copying (the copy generation number).

The real data tail in the data field contains 8-bit ID information and 16-bit management information. The real data tail  
15 also has an 8-bit reserved area. As shown in Fig. 14, the management information is composed of unit segments given addresses "00" to "FF" in hexadecimal notation respectively. The addresses "00" to "FF" are placed in the ID information in the real data tail. The management information has ISRC information in an  
20 address range from "00" to "07", an UPC/EAN/JAN code in an address range from "08" to "0B", SDCM information (copy management information) in an address "0C", encryption-related information in an address range from "0D" to "2F", allowed-use-term information (account information) in an address range from "30" to  
25 "3F", contents ID information in an address "40", copyright-protection-term information in an address range from "41" to "46",

player-related information in an address range from "47" to "4A",  
text data in address range from "4B" to "72", user ID information in  
an address range from "73" to "7F", a compression type flag (a DVD-  
Audio lossless compression flag or a DVD-Audio packed PCM flag) in  
5 an address "80", an reserved area in an address range from "81" to  
"BF", disc management data in an address range from "C0" to "C7",  
master tape management data in an address range from "C8" to  
"CE", and software production basic information in an address range  
from "CF" to "FF".

10 Multiple-channel audio data being AM824 data are placed in  
the real data within the data field. The multiple-channel audio data  
are in a 32-bit format shown in Fig. 15. With reference to Fig. 15,  
the multiple-channel audio data are composed of 32 bits, and have a  
sequence of a 3-bit head identifier "011", a 5-bit channel code, and  
15 24-bit PCM audio data. The 24-bit PCM audio data have 24-bit, 20-  
bit, or 16-bit effective data. In the case of 20-bit or 16-bit effective  
data, remaining bits starting from an LSB side are filled with "0".

As shown in Fig. 16, the channel code in the 32-bit multiple-  
channel audio data has words "0" to "1F" in hexadecimal notation.  
20 The code word "0" is assigned to a left front channel Lf. The code  
word "1" is assigned to a right front channel Rf. The code word "2"  
is assigned to a surround channel S. The code word "3" is assigned  
to a left surround channel Ls. The code word "4" is assigned to a  
right surround channel Rs. The code word "5" is assigned to a  
25 center channel C. The code word "6" is assigned to a low frequency  
effect channel LFE. The code words "7 to "1F" are reserved.

Fig. 17 shows an example of the real data which have a set of 32-bit segments each corresponding to 32-bit multiple-channel audio data (see Fig. 15).

#### Fourth Embodiment

5 A fourth embodiment of this invention is similar to the third embodiment thereof except for design changes indicated hereinafter. According to the fourth embodiment of this invention, 16-bit information is placed in ID information and a reserved area of a real data tail (see Fig. 13). The 16-bit information substitutes for  
10 the channel code in the 32-bit multiple-channel audio data (see Figs. 15 and 16).

As shown in Fig. 18, the 16-bit information has a sequence of 4-bit information of the audio sampling frequency  $f_s$  in the related channel group, 4-bit information of a multiple channel type (the  
15 sampling frequency and the bit number), 5-bit channel assignment information, and 3-bit data. The 3-bit data have a reserved bit, a down mix flag, and a dequantization flag. The channel assignment information is also referred to as the channel information.

The channel assignment represented by the 5-bit sequence in  
20 the channel assignment information can be changed among 21 different types shown in Fig. 19. A bit sequence of "00000" is allocated to a first type of the channel assignment in which a first channel ACH0 forms a monaural channel C(mono), and second and later channels ACH1, ACH2, ACH3, ACH4, and ACH5 are unused.  
25 According to the first type of the channel assignment, the monaural channel C(mono) is in the group "1". Thus, the channel number in



the group "1" is equal to one while the channel number in the group  
"2" is equal to zero. A bit sequence of "00001" is allocated to a  
second type of the channel assignment in which the first and  
second channels ACH0 and ACH1 form a left channel L and a right  
5 channel R respectively, and the third and later channels ACH2,  
ACH3, ACH4, and ACH5 are unused. According to the second type  
of the channel assignment, the left channel L and the right channel  
R are in the group "1". Thus, the channel number in the group "1"  
is equal to two while the channel number in the group "2" is equal  
10 to zero. A bit sequence of "00010" is allocated to a third type of the  
channel assignment in which the first, second, and third channels  
ACH0, ACH1, and ACH2 form a left front channel Lf, a right front  
channel Rf, and a surround channel S respectively, and the fourth  
and later channels ACH3, ACH4, and ACH5 are unused. According  
15 to the third type of the bit assignment, the left front channel Lf and  
the right front channel Rf are in the group "1" while the surround  
channel S is in the group "2". Thus, the channel number in the  
group "1" is equal to two while the channel number in the group "2"  
is equal to one. A bit sequence of "00011" is allocated to a fourth  
20 type of the channel assignment in which the first, second, third,  
and fourth channels ACH0, ACH1, ACH2, and ACH3 form a left front  
channel Lf, a right front channel Rf, a left surround channel Ls, and  
a right surround channel Rs respectively, and the fifth and sixth  
channels ACH4 and ACH5 are unused. According to the fourth type  
25 of the channel assignment, the left front channel Lf and the right  
front channel Rf are in the group "1" while the left surround

channel Ls and the right surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is also equal to two. A bit sequence of "00100" is allocated to a fifth type of the channel

5 assignment in which the first, second, and third channels ACH0, ACH1, and ACH2 form a left front channel Lf, a right front channel Rf, and a low frequency effect channel LFE respectively, and the fourth and later channels ACH3, ACH4, and ACH5 are unused.

According to the fifth type of the channel assignment, the left front  
10 channel Lf and the right front channel Rf are in the group "1" while the low frequency effect channel LFE is in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is equal to one. A bit sequence of "00101" is allocated to a sixth type of the channel assignment in which the  
15 first, second, third, and fourth channels ACH0, ACH1, ACH2, and ACH3 form a left front channel Lf, a right front channel Rf, a low frequency effect channel LFE, and a surround channel S

respectively, and the fifth and sixth channels ACH4 and ACH5 are unused. According to the sixth type of the channel assignment, the  
20 left front channel Lf and the right front channel Rf are in the group "1" while the low frequency effect channel LFE and the surround channel S are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is also equal to two. A bit sequence of "00110" is allocated to a  
25 seventh type of the channel assignment in which the first, second, third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and

ACH4 form a left front channel Lf, a right front channel Rf, a low frequency effect channel LFE, a left surround channel Ls, and a right surround channel Rs respectively, and the sixth channel ACH5 is unused. According to the seventh type of the channel assignment,

5 the left front channel Lf and the right front channel Rf are in the group "1" while the low frequency effect channel LFE, the left surround channel Ls, and the right surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is equal to three. A

10 bit sequence of "00111" is allocated to an eighth type of the channel assignment in which the first, second, and third channels ACH0, ACH1, and ACH2 form a left front channel Lf, a right front channel Rf, and a center channel C respectively, and the fourth and later channels ACH3, ACH4, and ACH5 are unused. According to the

15 eighth type of the channel assignment, the left front channel Lf and the right front channel Rf are in the group "1" while the center channel C is in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is equal to one. A bit sequence of "01000" is allocated to a ninth

20 type of the channel assignment in which the first, second, third, and fourth channels ACH0, ACH1, ACH2, and ACH3 form a left front channel Lf, a right front channel Rf, a center channel C, and a surround channel S respectively, and the fifth and sixth channels ACH4 and ACH5 are unused. According to the ninth type of the

25 channel assignment, the left front channel Lf and the right front channel Rf are in the group "1" while the center channel C and the

surround channel S are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is also equal to two. A bit sequence of "01001" is allocated to a tenth type of the channel assignment in which the first, second, 5 third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right front channel Rf, a center channel C, a left surround channel Ls, and a right surround channel Rs respectively, and the sixth channel ACH5 is unused. According to the tenth type of the channel assignment, the left front channel 10 Lf and the right front channel Rf are in the group "1" while the center channel C, the left surround channel Ls, and the right surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is equal to three. A bit sequence of "01010" is 15 allocated to an eleventh type of the channel assignment in which the first, second, third, and fourth channels ACH0, ACH1, ACH2, and ACH3 form a left front channel Lf, a right front channel Rf, a center channel C, and a low frequency effect channel LFE respectively, and the fifth and sixth channels ACH4 and ACH5 are 20 unused. According to the eleventh type of the channel assignment, the left front channel Lf and the right front channel Rf are in the group "1" while the center channel C and the low frequency effect channel LFE are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is also equal to two. A bit sequence of "01011" is allocated to a 25 twelfth type of the channel assignment in which the first, second,

third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right front channel Rf, a center channel C, a low frequency effect channel LFE, and a surround channel S respectively, and the sixth channel ACH5 is unused.

- 5 According to the twelfth type of the channel assignment, the left front channel Lf and the right front channel Rf are in the group "1" while the center channel C, the low frequency effect channel LFE, and the surround channel S are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number
- 10 in the group "2" is equal to three. A bit sequence of "01100" is allocated to a thirteenth type of the channel assignment in which the first, second, third, fourth, fifth, and sixth channels ACH0, ACH1, ACH2, ACH3, ACH4, and ACH5 form a left front channel Lf, a right front channel Rf, a center channel C, a low frequency effect
- 15 channel LFE, a left surround signal Ls, and a right surround channel Rs respectively. According to the thirteenth type of the channel assignment, the left front channel Lf and the right front channel Rf are in the group "1" while the center channel C, the low frequency effect channel LFE, the left surround signal Ls, and the right
- 20 surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to two while the channel number in the group "2" is equal to four. A bit sequence of "01101" is allocated to a fourteenth type of the channel assignment in which the first, second, third, and fourth channels ACH0, ACH1, ACH2,
- 25 and ACH3 form a left front channel Lf, a right front channel Rf, a center channel C, and a surround channel S respectively, and the

fifth and sixth channels ACH4 and ACH5 are unused. According to the fourteenth type of the channel assignment, the left front channel Lf, the right front channel Rf, and the center channel C are in the group "1" while the surround channel S is in the group "2".

- 5 Thus, the channel number in the group "1" is equal to three while the channel number in the group "2" is equal to one. A bit sequence of "01110" is allocated to a fifteenth type of the channel assignment in which the first, second, third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right  
10 front channel Rf, a center channel C, a left surround channel Ls, and a right surround channel Rs respectively, and the sixth channel ACH5 is unused. According to the fifteenth type of the channel assignment, the left front channel Lf, the right front channel Rf, and the center channel C are in the group "1" while the left surround  
15 channel Ls and the right surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to three while the channel number in the group "2" is equal to two. A bit sequence of "01111" is allocated to a sixteenth type of the channel assignment in which the first, second, third, and fourth channels ACH0, ACH1,  
20 ACH2, and ACH3 form a left front channel Lf, a right front channel Rf, a center channel C, and a low frequency effect channel LFE respectively, and the fifth and sixth channels ACH4 and ACH5 are unused. According to the sixteenth type of the channel assignment, the left front channel Lf, the right front channel Rf, and the center  
25 channel C are in the group "1" while the low frequency effect channel LFE is in the group "2". Thus, the channel number in the

group "1" is equal to three while the channel number in the group "2" is equal to one. A bit sequence of "10000" is allocated to a seventeenth type of the channel assignment in which the first, second, third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right front channel Rf, a center channel C, a low frequency effect channel LFE, and a surround channel S respectively, and the sixth channel ACH5 is unused. According to the seventeenth type of the channel assignment, the left front channel Lf, the right front channel Rf, and the center channel C are in the group "1" while the low frequency effect channel LFE and the surround channel S are in the group "2". Thus, the channel number in the group "1" is equal to three while the channel number in the group "2" is equal to two. A bit sequence of "10001" is allocated to an eighteenth type of the channel assignment in which the first, second, third, fourth, fifth, and sixth channels ACH0, ACH1, ACH2, ACH3, ACH4, and ACH5 form a left front channel Lf, a right front channel Rf, a center channel C, a low frequency effect channel LFE, a left surround signal Ls, and a right surround channel Rs respectively. According to the eighteenth type of the channel assignment, the left front channel Lf, the right front channel Rf, and the center channel C are in the group "1" while the low frequency effect channel LFE, the left surround signal Ls, and the right surround channel Rs are in the group "2". Thus, the channel number in the group "1" is equal to three while the channel number in the group "2" is also equal to three. A bit sequence of "10010" is allocated to a nineteenth type of the channel assignment

in which the first, second, third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right front channel Rf, a left surround channel Ls, a right surround channel Rs, and a low frequency effect channel LFE respectively,  
5 and the sixth channel ACH5 is unused. According to the nineteenth type of the channel assignment, the left front channel Lf, the right front channel Rf, the left surround channel Ls, and the right surround channel Rs are in the group "1" while the low frequency effect channel LFE is in the group "2". Thus, the channel number in  
10 the group "1" is equal to four while the channel number in the group "2" is equal to one. A bit sequence of "10011" is allocated to a twentieth type of the channel assignment in which the first, second, third, fourth, and fifth channels ACH0, ACH1, ACH2, ACH3, and ACH4 form a left front channel Lf, a right front channel Rf, a left  
15 surround channel Ls, a right surround channel Rs, and a center channel C respectively, and the sixth channel ACH5 is unused. According to the twentieth type of the channel assignment, the left front channel Lf, the right front channel Rf, the left surround channel Ls, and the right surround channel Rs are in the group "1"  
20 while the center channel C is in the group "2". Thus, the channel number in the group "1" is equal to four while the channel number in the group "2" is equal to one. A bit sequence of "10100" is allocated to a twenty-first type of the channel assignment in which the first, second, third, fourth, fifth, and sixth channels ACH0,  
25 ACH1, ACH2, ACH3, ACH4, and ACH5 form a left front channel Lf, a right front channel Rf, a left surround channel Ls, a right surround



channel Rs, a center channel C, and a low frequency effect channel LFE respectively. According to the twenty-first type of the channel assignment, the left front channel Lf, the right front channel Rf, the left surround channel Ls, and the right surround channel Rs are in the group "1" while the center channel C and the low frequency effect channel C are in the group "2". Thus, the channel number in the group "1" is equal to four while the channel number in the group "2" is equal to two.

The fourth embodiment of this invention uses a 32-bit multiple-channel audio data format in Fig. 20 instead of the 32-bit multiple-channel audio data format in Fig. 15. With reference to Fig. 20, 32-bit multiple-channel audio data have a sequence of a 3-bit head identifier "011", a 5-bit flag, and 24-bit PCM audio data. The 24-bit PCM audio data have 24-bit, 20-bit, or 16-bit effective data. In the case of 20-bit or 16-bit effective data, remaining bits starting from an LSB side are filled with "0". The 5-bit flag is designed as follows. The two higher bits of the 5-bit flag represent the bit number of effective data in the 24-bit PCM audio data. Specifically, the two higher bits being "00" indicate that the effective data in the 24-bit PCM audio data have 24 bits. The two higher bits being "01" indicate that the effective data in the 24-bit PCM audio data have 20 bits. The two higher bits being "10" indicate that the effective data in the 24-bit PCM audio data have 16 bits. The three lower bits of the 5-bit flag represent down sampling information. Specifically, the three lower bits being "010" indicate the absence of down sampling. Here, "down sampling" means, for example, change to a

1/2 sampling frequency, that is, halving a sampling frequency. The three lower bits being "011" indicate the presence of down sampling. The channel corresponding to the 32-bit multiple-channel audio data is decided by the channel assignment

5 information in Fig. 18 and the order of the channels ACH0, ACH1, ACH2, ACH3, ACH4, and ACH5 in Fig. 19.

#### Fifth Embodiment

Fig. 21 shows a disc player according to a fifth embodiment of this invention. The disc player in Fig. 21 can be used as the disc  
10 player 100 or 100A in one of the first, second, third, and fourth embodiments of this invention. The disc player in Fig. 21 is of a universal type, being able to play back any one of different-type DVD's including a DVD-Audio and a DVD-Video.

The disc player in Fig. 21 operates on a DVD family disc (for  
15 example, a DVD-Audio, a DVD-Video, or a DVD-RAM) 1. As shown in Fig. 21, the disc player includes a control unit 14, an operation unit 15, and a remote control unit 16. The remote control unit 16 can communicate with the operation unit 15 by wireless. The operation unit 15 is connected to the control unit 14. The control unit 14  
20 includes a CPU. The control unit 14 is connected to a disc drive unit 2 and a reproduced signal processing unit 50. The disc drive unit 2 is connected to the reproduced signal processing unit 50 via a demodulation circuit 2B.

The CPU 14 operates in accordance with a control program  
25 stored in its internal ROM. When the user actuates the operation unit 15 or the remote control unit 16 to request tune selection,

playback, fast feed, or stop, the CPU 14 controls the disc drive unit 2 and the reproduced signal processing unit 50 to implement the requested operation mode.

During playback, the disc drive unit 2 reads out a signal from the DVD family disc 1. The disc drive unit 2 outputs the read-out signal to the demodulation circuit 2B. The demodulation circuit 2B subjects the output signal of the disc drive unit 2 to given demodulation (for example, EFM demodulation). The demodulation circuit 2B outputs the demodulation-resultant signal to the reproduced signal processing unit 50 as a reproduced signal. The demodulation circuit 2B may be incorporated in the disc drive unit 2.

The reproduced signal processing circuit 50 includes a video and still-picture pack decoder 3 receiving the reproduced signal from the demodulation circuit 2B. The video and still-picture pack decoder 3 includes a detector for detecting video packs and still picture packs in the reproduced signal. The video and still-picture pack decoder 3 decodes the video packs and the still picture packs into a video data stream (a DVD video stream). The video and still-picture pack decoder 3 outputs the video data stream to an expansion video converter 4 and data transmission interfaces 7-1 and 7-2 in the reproduced signal processing circuit 50. The data transmission interfaces 7-1 and 7-2 correspond to the data transmission interface 200a in Fig. 1 or the data transmission interface 200aa in Fig. 3.

The expansion video converter 4 subjects the video data

stream to an expansion process and a descrambling process, thereby converting the video data stream into a digital video signal. The expansion video converter 4 outputs the digital video signal to a D/A converter 5 in the reproduced signal processing circuit 50.

- 5 The D/A converter 5 changes the digital video signal into a corresponding analog video signal. The D/A converter 5 outputs the analog video signal. The analog video signal can be an analog sub picture signal originating from the video packs. The analog video signal can be transmitted from the D/A converter 5 to an external display. In this case, pictures represented by the analog video signal are indicated by the external display.

- The expansion video converter 4 outputs the digital video signal also to a data arranger 6 in the reproduced signal processing circuit 50. The data arranger 6 subjects the digital video signal to a packing-based data arranging process, thereby converting the digital video signal into an arrangement-resultant signal. The data arranger 6 outputs the arrangement-resultant digital video signal to the data transmission interfaces 7-1 and 7-2.

- During a first video transmission mode of operation of the disc player, the arrangement-resultant digital video signal is sent from the data arranger 6 to a recording and reproducing device 200 (see Fig. 1 or 3) via the data transmission interfaces 7-1 and 7-2 and IEEE1394 serial interfaces. The IEEE1394 serial interfaces correspond to the IEEE1394 serial interfaces 188-1, 188-2, 188-3, and 188-4 in Fig. 1 or 3. The IEEE1394 serial interfaces may be replaced by IEC958 serial interfaces.

During a second video transmission mode of operation of the disc player, the video data stream is sent from the video and still-picture pack decoder 3 to the recording and reproducing device 200 via the data transmission interfaces 7-1 and 7-2 and the

5 IEEE1394 serial interfaces (or the IEC958 serial interfaces).

The reproduced signal processing circuit 50 includes an audio and real-time-information pack decoder 8 receiving the reproduced signal from the demodulation circuit 2B. The audio and real-time-information pack decoder 8 includes a detector for detecting audio

10 packs and real time information packs in the reproduced signal. The audio and real-time-information pack decoder 8 decodes the audio packs into an audio data stream (a DVD audio stream). The audio and real-time-information pack decoder 8 outputs the audio data stream to an audio signal processor 9 and data transmission

15 interfaces 13-1 and 13-2 in the reproduced signal processing circuit 50. The data transmission interfaces 13-1 and 13-2 correspond to the data transmission interface 200a in Fig. 1 or the data transmission interface 200aa in Fig. 3.

The audio signal processor 9 subjects the audio data stream to

20 a PCM conversion process and a descrambling process, thereby converting the audio data stream into a PCM audio signal. The audio signal processor 9 may also subjects the audio data stream to a dequantization process. The audio signal processor 9 outputs the PCM audio signal to a down sampler 10 and a D/A converter 17 in

25 the reproduced signal processing circuit 50. The D/A converter 17 changes the PCM audio signal into a corresponding analog audio

signal. The analog audio signal can be transmitted from the D/A converter 17 to an external loudspeaker. In this case, the analog audio signal is converted into corresponding sounds by the external loudspeaker.

5           The audio and real-time-information pack decoder 8 extracts real time information (for example, character information or real time text information) from the real time information packs. The audio and real-time-information pack decoder 8 outputs the real time information to a display signal generator 18 in the reproduced  
10           signal processing circuit 50. The display signal generator 18 converts the real time information into a corresponding display signal. The display signal can be transmitted from the display signal generator 18 to the external display. In this case, the external display indicates the display signal. The display signal contains, for  
15           example, the character information or the real time text information.

          The down sampler 10 subjects the PCM audio signal to a down sampling process, thereby converting the PCM audio signal into a down-sampling-resultant digital audio signal. The down sampler 10  
20           outputs the down-sampling-resultant digital audio signal to a down mixer 11 in the reproduced signal processing circuit 50. In the case where the output signal of the down sampler 10 is a multiple-channel signal, the device 11 down-mixes the output signal of the down sampler 10 into a stereophonic 2-channel digital audio signal.  
25           The down mixer 11 outputs the stereophonic 2-channel digital audio signal to a data arranger 12 in the reproduced signal

processing circuit 50. In the case where the output signal of the down sampler 10 is a monaural signal or a 2-channel signal, the down mixer 11 passes the signal from the down sampler 10 to the data arranger 12 without processing the signal. The data arranger 12 subjects the output signal of the down mixer 11 to a data  
5 arranging process, thereby converting the output signal of the down mixer 11 into an arrangement-resultant digital audio signal. The data arranger 12 outputs the arrangement-resultant digital audio signal to the data transmission interfaces 13-1 and 13-2.

10 During a first audio transmission mode of operation of the disc player, the arrangement-resultant digital audio signal is sent from the data arranger 12 to the recording and reproducing device 200 (see Fig. 1 or 3) via the data transmission interfaces 13-1 and 13-2 and IEEE1394 serial interfaces. The IEEE1394 serial interfaces  
15 correspond to the IEEE1394 serial interfaces 188-1, 188-2, 188-3, and 188-4 in Fig. 1 or 3. The IEEE1394 serial interfaces may be replaced by IEC958 serial interfaces.

During a second audio transmission mode of operation of the disc player, the audio data stream is sent from the audio and real-  
20 time-information pack decoder 8 to the recording and reproducing device 200 via the data transmission interfaces 13-1 and 13-2 and the IEEE1394 serial interfaces (or the IEC958 serial interfaces).

#### Sixth Embodiment

Fig. 22 shows a recording and reproducing device according  
25 to a sixth embodiment of this invention. The recording and reproducing device in Fig. 22 can be used as the recording and

reproducing device 200 in one of the first, second, third, and fourth embodiments of this invention.

As shown in Fig. 22, the recording and reproducing device includes a control unit 32 and an operation unit 33. The operation  
5 unit 33 is connected to the control unit 32. The control unit 32 includes a CPU. The control unit 32 is connected to an account management unit 34 and a signal processing unit 60.

The CPU 32 operates in accordance with a control program stored in an internal ROM. When the user actuates the operation  
10 unit 33 to request an operation mode (for example, a playback operation mode), the CPU 32 controls the signal processing unit 60 to implement the requested operation mode.

The signal processing unit 60 includes data transmission interfaces 21-1 and 21-2 which correspond to the data transmission  
15 interface 200b in Fig. 1 or the data transmission interface 200bb in Fig. 3. The data transmission interfaces 21-1 and 21-2 can receive data from the disc player 100 or 100A (see Fig. 1 or Fig. 3) via the IEEE1394 serial interfaces. The IEEE1394 serial interfaces may be replaced by the IEC958 serial interfaces.

20 The signal processing unit 60 includes a DVD decoder 22, an audio pack processor 23, a video pack processor 24, a real time information pack processor 25, and a still picture pack processor 26 which follow the data transmission interfaces 21-1 and 21-2. The signal processing unit 60 further includes buffers 22V, 23V,  
25 24V, 25V, and 26V connected to the decoder and processors 22, 23, 24, 25, and 26 respectively.



The control unit 32 looks into a 4-bit pack flag, that is, a 4-bit signal type flag in application information within each real data header (see Fig. 13) of the data received by the data transmission interfaces 21-1 and 21-2. The control unit 32 operates to distribute the received data from the data transmission interfaces 21-1 and 21-2 to the buffers 22V, 23V, 24V, 25V, and 26V in accordance with the pack flag. Specifically, when the pack flag represents that the received data corresponds to an audio pack, the control unit 32 acts to transfer the received data from the data transmission interfaces 21-1 and 21-2 into the audio pack buffer 23V. When the pack flag represents that the received data corresponds to a video pack, the control unit 32 acts to transfer the received data from the data transmission interfaces 21-1 and 21-2 into the video pack buffer 24V. When the pack flag represents that the received data corresponds to a real time information pack, the control unit 32 acts to transfer the received data from the data transmission interfaces 21-1 and 21-2 into the real time information pack buffer 25V. When the pack flag represents that the received data corresponds to a still picture pack, the control unit 32 acts to transfer the received data from the data transmission interfaces 21-1 and 21-2 into the still picture pack buffer 26V. In the absence of an effective pack flag, the control unit 32 acts to transfer the received data from the data transmission interfaces 21-1 and 21-2 into the DVD buffer 22V.

25        The control unit 32 looks into contents ID information in management information within each real data tail (see Figs. 13 and

14) of the received data. The control unit 32 identifies contents information in the received data by referring to the contents ID information. The control unit 32 controls the account management unit 34 to implement an account process in response to the result  
5 of the identification of the contents information.

Alternatively, the control unit 32 may look into an account flag in the application information within each real data header (see Fig. 13) of the received data. When the account flag represents that contents information in the received data corresponds to "paid", the  
10 control unit 32 controls the account management unit 34 to implement an account process.

The control unit 32 looks into user ID information in management information within each real data tail (see Figs. 13 and 14) of the received data. The user ID information is designed for a  
15 specified user. The control unit 32 receives information of the present user from the operation unit 33. The control unit 32 decides whether or not the present user is equal to the specified user.

In the case where audio playback is requested by user's  
20 command inputted via the operation unit 33, the control unit 32 looks into a packed PCM flag in the management information within each real data tail (see Figs. 13 and 14) of the received data. When the packed PCM flag is active, the control unit 32 controls the audio pack processor 23 to expand and decode audio data in the audio  
25 pack buffer 22V. Accordingly, it is possible to decide whether or not the audio data expansion should be done by referring to only the

packed PCM flag rather than the whole of the received data. Thus, a high data reproduction efficiency is provided. In addition, a smaller buffer capacity suffices.

In the case where the disc player is incapable of executing  
5 audio data expansion, the control unit 32 controls the data transmission interfaces 21-1 and 21-2 to suspend the data reception.

The DVD decoder 22 accesses data in the buffer 22V, and decodes the accessed data into an original signal. The DVD decoder  
10 22 outputs the original signal to a terminal 27. The original signal can be transmitted to an external device through the terminal 27.

The audio pack processor 23 accesses audio data in the buffer 23V, and processes and decodes the accessed audio data into an audio signal. The audio pack processor 23 outputs the audio signal  
15 to a terminal 28. The audio signal can be transmitted to an external device through the terminal 28.

The audio pack processor 23 includes latch circuits 23a corresponding to a left front channel Lf, a right front channel Rf, a surround channel S, a left surround channel Ls, a right surround  
20 channel Rs, a center channel C, and a low frequency effect channel LFE respectively. The audio pack processor 23 demultiplexes the accessed audio data into an Lf-channel audio data, an Rf-channel audio data, an S-channel audio data, an Ls-channel audio data, a Rs-channel audio data, a C-channel audio data, and an LFE-channel  
25 audio data. The audio pack processor 23 stores the Lf-channel audio data, the Rf-channel audio data, the S-channel audio data, the Ls-

channel audio data, the Rs-channel audio data, the C-channel audio data, and the LFE-channel audio data into the corresponding latch circuits 23a, respectively. The Lf-channel audio data, the Rf-channel audio data, the S-channel audio data, the Ls-channel audio data, the Rs-channel audio data, the C-channel audio data, and the LFE-channel audio data are outputted from the latch circuits 23a to the terminal 28 as separate signals while being synchronized with each other in response to a sync signal.

The video pack processor 24 accesses video data in the buffer 24V, and processes and decodes the accessed video data into a video signal. The video pack processor 24 outputs the video signal to a terminal 29. The video signal can be transmitted to an external device through the terminal 29.

The real time information pack processor 25 accesses real time information data in the buffer 25V, and processes and decodes the accessed real time information data into a real time information signal. The real time information pack processor 25 outputs the real time information signal to a terminal 30. The real time information signal can be transmitted to an external device through the terminal 30.

The still picture pack processor 26 accesses still picture data in the buffer 26V, and processes and decodes the accessed still picture data into a still picture signal. The still picture pack processor 26 outputs the still picture signal to a terminal 31. The still picture signal can be transmitted to an external device through the terminal 31.

The 4-bit pack flag in the application information within each real data header (see Fig. 13) of the received data enables the disc player to immediately decode audio data, still picture data, real time information data, and video data. Thus, it is unnecessary to store a  
5 great amount of still picture data in the buffer 26V to synchronize played-back still picture information with played-back audio information. Accordingly, the synchronized-playback-related limitation on still pictures is eased. Furthermore, it is possible to simultaneously play back audio-added moving pictures and audio  
10 information.

The control unit 32 looks into a zero flag in the application information within each real data header (see Fig. 13) of the received data. When the zero flag is active, the control unit 32 controls the data transmission interfaces 21-1 and 21-2 to prevent  
15 the execution of a main-data receiving process.

The control unit 32 looks into a mute flag in the application information within each real data header (see Fig. 13) of the received data. When the mute flag is active, the control unit 32 controls the audio pack processor 23 to execute a muting process  
20 on audio data.

The control unit 32 extracts an account flag from the application information within each real data header (see Fig. 13) of the received data. In addition, the control unit 32 extracts allowed-use-term information (account information) and contents ID  
25 information from the management information within each real data tail (see Figs. 13 and 14) of the received data. The control unit 32

transfers the account flag, the account information, and the contents ID information to the account management unit 34. The account flag, the account information, and the contents ID information are processed by the account management unit 34.

- 5 Generally, SDCM information (copy management information) in the application information is unused.

The audio pack processor 23 operates in accordance with a control program stored in its internal ROM. Fig. 23 is a flowchart of a segment of the control program which is executed for every 32-bit multiple-channel audio data in the format of Fig. 15.

As shown in Fig. 23, a first step S40 of the program segment decides whether or not three front bits of 32-bit incoming audio data are equal to "011". When the three front bits of the 32-bit incoming audio data are equal to "011", the program advances from the step S40 to a step S41. Otherwise, the program exits from the step S40 and then the current execution cycle of the program segment ends.

The step S41 detects a channel code in the 32-bit incoming audio data. After the step S41, the program advances to a step S42.

The step S42 decides whether or not the channel code corresponds to a left front channel Lf. When the channel code corresponds to the left front channel Lf, the program advances from the step S42 to a step S43. Otherwise, the program advances from the step S42 to a step S44.

The step S43 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the left front

The step S44 decides whether or not the channel code corresponds to a right front channel Rf. When the channel code corresponds to the right front channel Rf, the program advances from the step S44 to a step S45. Otherwise, the program advances from the step S44 to a step S46.

The step S46 decides whether or not the channel code corresponds to a left surround channel Ls. When the channel code corresponds to the left surround channel Ls, the program advances from the step S46 to a step S47. Otherwise, the program advances  
15 from the step S46 to a step S48.

20           The step S48 decides whether or not the channel code corresponds to a right surround channel Rs. When the channel code corresponds to the right surround channel Rs, the program advances from the step S48 to a step S49. Otherwise, the program advances from the step S48 to a step S50.

surround channel Rs. After the step S49, the program advances to the step S54.

The step S50 decides whether or not the channel code corresponds to a center channel C. When the channel code  
5 corresponds to the center channel C, the program advances from the step S50 to a step S51. Otherwise, the program advances from the step S50 to a step S52.

The step S51 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the center  
10 channel C. After the step S51, the program advances to the step S54.

The step S52 decides whether or not the channel code corresponds to a low frequency effect channel LFE. When the channel code corresponds to the low frequency effect channel LFE,  
15 the program advances from the step S52 to a step S53. Otherwise, the program advances from the step S52 to the step S54.

The step S53 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the low frequency effect channel LFE. After the step S53, the program  
20 advances to the step S54.

The step S54 decides whether or not the channel-code-responsive process has been completed. When the channel-code-responsive process has been completed, the program exits from the step S54 and then the current execution cycle of the program  
25 segment ends. When the channel-code-responsive process has not been completed yet, the program returns from the step S54 to the



step S41.

Fig. 24 is a flowchart of a program segment which can replace the program segment in Fig. 23. The program segment in Fig. 24 is executed for every 32-bit multiple-channel audio data in the format of Fig. 20.

As shown in Fig. 24, a first step S60 of the program segment decides whether or not three front bits of 32-bit incoming audio data are equal to "011". When the three front bits of the 32-bit incoming audio data are equal to "011", the program advances from the step S60 to a step S61. Otherwise, the program exits from the step S60 and then the current execution cycle of the program segment ends.

The step S61 detects a 5-bit flag in the 32-bit incoming audio data. The step S61 detects channel assignment information (see Fig. 18) in the received data. The step S61 retrieves information of the order of the channels ACH0, ACH1, ACH2, ACH3, ACH4, and ACH5 (see Fig. 19). After the step S61, the program advances to a step S62.

The step S62 decides whether or not the 32-bit incoming audio data correspond to a left front channel Lf on the basis of the channel assignment information and the channel order information. When the 32-bit incoming audio data correspond to the left front channel Lf, the program advances from the step S62 to a step S63. Otherwise, the program advances from the step S62 to a step S64.

The step S63 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the left front

channel Lf.

A step S80 following the step S63 sets the value of the sampling frequency  $f_s$  of the left front channel Lf in response to down sampling information represented by the three lower bits of the 5-bit flag. Specifically, the step S80 halves the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S80, the program advances to a step S74.

The step S64 decides whether or not the 32-bit incoming audio data correspond to a right front channel Rf on the basis of the channel assignment information and the channel order information. When the 32-bit incoming audio data correspond to the right front channel Rf, the program advances from the step S64 to a step S65. Otherwise, the program advances from the step S64 to a step S66.

The step S65 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the right front channel Rf.

A step S81 following the step S65 sets the value of the sampling frequency  $f_s$  of the right front channel Rf in response to the down sampling information. Specifically, the step S81 halves the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S81, the program advances to the step S74.

The step S66 decides whether or not the 32-bit incoming audio data correspond to a left surround channel Ls on the basis of the channel assignment information and the channel order

information. When the 32-bit incoming audio data correspond to the left surround channel Ls, the program advances from the step S66 to a step S67. Otherwise, the program advances from the step S66 to a step S68.

- 5           The step S67 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the left surround channel Ls.

- 10           A step S82 following the step S67 sets the value of the sampling frequency  $f_s$  of the left surround channel Ls in response to the down sampling information. Specifically, the step S82 halves the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S82, the program advances to the step S74.

- 15           The step S68 decides whether or not the 32-bit incoming audio data correspond to a right surround channel Rs on the basis of the channel assignment information and the channel order information. When the 32-bit incoming audio data correspond to the right surround channel Rs, the program advances from the step S68 to a step S69. Otherwise, the program advances from the step  
20           S68 to a step S70.

The step S69 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the right surround channel Rs.

- 25           A step S83 following the step S69 sets the value of the sampling frequency  $f_s$  of the right surround channel Rs in response to the down sampling information. Specifically, the step S83 halves

the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S83, the program advances to the step S74.

5       The step S70 decides whether or not the 32-bit incoming audio data correspond to a center channel C on the basis of the channel assignment information and the channel order information. When the 32-bit incoming audio data correspond to the center channel C, the program advances from the step S70 to a step S71. Otherwise, the program advances from the step S70 to a step S72.

10       The step S71 stores PCM audio data of the 32-bit incoming audio data into the latch circuit 32a corresponding to the center channel C.

15       A step S84 following the step S71 sets the value of the sampling frequency  $f_s$  of the center channel C in response to the down sampling information. Specifically, the step S84 halves the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S84, the program advances to the step S74.

20       The step S72 decides whether or not the 32-bit incoming audio data correspond to a low frequency effect channel LFE on the basis of the channel assignment information and the channel order information. When the 32-bit incoming audio data correspond to the low frequency effect channel LFE, the program advances from the step S72 to a step S73. Otherwise, the program advances from  
25       the step S72 to the step S74.

      The step S73 stores PCM audio data of the 32-bit incoming

audio data into the latch circuit 32a corresponding to the low frequency effect channel LFE.

5 A step S85 following the step S73 sets the value of the sampling frequency  $f_s$  of the low frequency effect channel LFE in response to the down sampling information. Specifically, the step S85 halves the sampling frequency  $f_s$  when the down sampling information indicates the presence of down sampling. After the step S85, the program advances to the step S74.

10 The step S74 decides whether or not the channel-assignment-responsive process has been completed. When the channel-assignment-responsive process has been completed, the program exits from the step S74 and then the current execution cycle of the program segment ends. When the channel-assignment-responsive process has not been completed yet, the program  
15 returns from the step S74 to the step S61.

#### Seventh Embodiment

A signal processing apparatus of a seventh embodiment of this invention is similar to the signal processing apparatus in Fig. 1 except for design changes indicated hereinafter. The signal  
20 processing apparatus of the seventh embodiment of this invention includes the disc player in Fig. 21 which replaces the disc player 100. It should be noted that the signal processing apparatus of the seventh embodiment of this invention may include the disc player 100.

25 The control unit 14 in the disc player of Fig. 21 can directly access the serial interfaces 188-1 and 188-2 (see Fig. 1).

Alternatively, the control unit 14 can access the serial interfaces 188-1 and 188-2 via the data transmission interfaces 7-1, 7-2, 13-1, and 13-2. The control unit 14 is able to control the data transmission interfaces 7-1, 7-2, 13-1, and 13-2, and the serial  
5 interfaces 188-1 and 188-2.

Fig. 25 is a flowchart of a segment of a control program for the control unit 14. With reference to Fig. 25, a first step S141 of the program segment sets the serial interface 188-1 in a receiving operation mode as viewed from the disc player. Furthermore, the  
10 step S141 sets the serial interface 188-2 in a transmitting operation mode as viewed from the disc player. After the step S141, the program advances to a step S142.

The step S142 decides whether or not transmission is requested by a recording and reproducing apparatus 200. When  
15 transmission is requested, the program advances from the step S142 to a step S143. Otherwise, the step S142 is repeated.

The step S143 receives a signal of the transmission request. A step S144 following the step S143 sets and controls the down sampler 10 (see Fig. 21) to implement a down sampling process.  
20 The step S144 sets and controls the audio signal processor 9 (see Fig. 21) to implement a dequantization process.

A step S145 subsequent to the step S144 decides whether or not a down mix process is required. When the down mix process is required, the program advances from the step S145 to a step S146.  
25 Otherwise, the program jumps from the step S145 to a step S147.

The step S146 sets and controls the down mixer 11 to

implement the down mix process. After the step S146, the program advances to the step S147.

The step S147 implements two-way communications with the data transmission interface 200b (the recording and reproducing device 200) via the data transmission interfaces 7-1, 7-2, 13-1, and 13-2, and the serial interfaces 188-1 and 188-2. During the two-way communications, operation command signals such as a play command signal and a disc request signal are transmitted.

A step S148 subsequent to the step S147 changes the serial  
10 interface 188-1 to a transmitting operation mode as viewed from  
the disc player.

A step S149 following the step S148 transmits plural signals to the data transmission interface 200b (the recording and reproducing device 200) via the data transmission interfaces 7-1, 7-2, 13-1, and 13-2, and the serial interfaces 188-1 and 188-2. The plural signals include high-transfer-rate signals reproduced from a DVD-Audio. Examples of the high-transfer-rate signals are an audio signal, a real time information signal, and a still picture signal. The step S149 assigns the transmission of at least first one of the high-transfer-rate signals to the serial interface 188-1. The step S149 assigns the transmission of at least second one of the high-transfer-rate signals to the serial interface 188-2. For example, the transmission of an audio signal is assigned to the serial interface 188-1 while the transmission of a real time information signal and a still picture signal is assigned to the serial interface 188-2. In the case where the audio signal, the real time information signal, and

the still picture signal are synchronously reproduced, the distributed signal transmission removes a significant limitation on a buffer capacity so that a lot of still pictures (for example, 80 to 99 still pictures) can be reproduced synchronously with audio  
5 information.

A step S150 subsequent to the step S149 decides whether or not the signal transmission has been completed. When the signal transmission has been completed, the program exits from the step S150 and then the current execution cycle of the program segment  
10 ends. When the signal transmission has not been completed yet, the program returns from the step S150 to the step S141.

The disc player sends various flags and copy-related information to the recording and reproducing device 200 as in the third embodiment of this invention. The various flags include an  
15 account flag, a zero flag, a mute flag, a pack flag (a pack type flag or a signal type flag), and a copy flag. The copy-related information includes a down sampling flag Fa, a down mix flag Fb, a dequantization flag Fc, and an information piece representing the number of times of copying (the copy generation number). It should  
20 be noted that a down mix flag and a dequantization flag may be sent in a way as in the fourth embodiment of this invention (see Fig. 18).

The controller within the recording and reproducing device 200 can access the data transmission interface 200b (see Fig. 1). The controller can access the serial interfaces 188-1 and 188-2 via  
25 the data transmission interface 200b. Alternatively, the controller can directly access the serial interfaces 188-1 and 188-2. The



controller is able to control the data transmission interface 200b and the serial interfaces 188-1 and 188-2.

Fig. 26 is a flowchart of a segment of a control program for the controller within the recording and reproducing device 200. With  
5 reference to Fig. 26, a first step S151 of the program segment sets the serial interface 188-1 in a transmitting operation mode as viewed from the recording and reproducing device 200.

Furthermore, the step S151 sets the serial interface 188-2 in a  
10 receiving operation mode as viewed from the recording and reproducing device 200.

A step S152 following the step S151 requests transmission of the disc player by using the serial interface 188-1. The step S152 may implement an authentication process as follows. The step S152 receives authentication data from the disc player via the serial  
15 interface 188-2 after transmission has been requested. The step S152 generates an authentication response signal when receiving the authentication data. The step S152 sends the authentication response signal to the disc player via the serial interface 188-1. In the case where the authentication response signal represents that  
20 the recording and reproducing device 200 is authorized to make a copy of main data, the disc player starts the transmission of main data (contents information).

A step S153 subsequent to the step S152 changes the serial interface 188-1 to a receiving operation mode as viewed from the  
25 recording and reproducing device 200.

A step S154 following the step S153 receives signals from the

disc player via the serial interfaces 188-1 and 188-2. After the step S154, the program advances to a step S155.

The step S155 detects received flags and received copy-related information. The step S155 looks into an account flag  
5 among the detected flags. When the account flag represents that contents information corresponds to "paid", the step S155 refers to a received copy-related information piece representative of the number of times of copying and determines a charge in response to the number of times of copying. Then, the step S155 executes  
10 account management including a process of electronically paying the charge from an electronic purse provided in the reception side.

A step S156 following the step S155 decides whether or not a zero flag of "1" is received. When a zero flag of "1" is received, the program advances from the step S156 to a step S157. Otherwise,  
15 the program advances from the step S156 to a step S158.

The step S157 prevents a main-data receiving process from being executed. After the step S157, the program advances to a step S161.

The step S158 decides whether or not a mute flag of "1" (or  
20 mute flags of "1") is received. When a mute flag of "1" is received, the program advances from the step S158 to a step S159. Otherwise, the program advances from the step S158 to a block S160.

The step S159 executes a muting process on a signal received  
25 via the serial interface related to the mute flag of "1". After the step S159, the program advances to the block S160.

The block S160 looks into a signal type flag (a pack flag) among the received flags, and executes the main-data receiving process in response to the signal type flag. After the block S160, the program advances to the step S161. The block S160 is similar  
5 to the block S26 in Figs. 5 and 6.

The step S161 decides whether or not the reception of the main data has been completed. When the reception of the main data has been completed, the program exits from the step S161 and then the current execution cycle of the program segment ends.  
10 When the reception of the main data has not been completed yet, the program returns from the step S161 to the step S155.

The recording and reproducing device 200 notifies the disc player of the completion of the reception of the main data, that is, the completion of copying the main data. The disc player updates  
15 or counts up the number of times of copying, which is represented by the piece of the copy-related information, in response to the notice from the recording and reproducing device 200.

#### Eighth Embodiment

An eighth embodiment of this invention is similar to one of  
20 the first, second, third, fourth, and seventh embodiments thereof except for design changes indicated hereinafter. According to the eighth embodiment of this invention, PCM audio data in Fig. 7 or Fig. 8 are replaced by audio data resulting from a 1-bit DSD encoding process. Here, DSD is short for "Direct Stream Digital".

25 The eighth embodiment of this invention uses a 32-bit audio data format in Fig. 27 which replaces the 32-bit audio data format in

Fig. 15 or Fig. 20. The eighth embodiment of this invention may use a data format conforming to the SACD (super audio CD) standards. With reference to Fig. 27, 32-bit audio data have a sequence of a 3-bit head identifier "111", a 1-bit encoding-related flag, 4-bit channel  
5 bit information, and 24-bit DSD audio data. The encoding-related flag represents whether lossless encoding (for example, compressive lossless encoding or Huffman encoding) is used or not in connection with the DSD audio data. The channel bit information is divided into 2-bit channel-number information and 2-bit bit-  
10 number information. The channel-number information being "00" indicates two channels. The channel-number information being "01" indicates three channels. The channel-number information being "10" indicates six channels. The channel-number information being "11" indicates another number of channels. The bit-number  
15 information represents the number of bits composing each audio information sample.

It should be noted that a DSD flag corresponding to the DSD head identifier "111" may be placed in the reserved area of the management information in Fig. 14.

20 The eighth embodiment of this invention uses a block S26A in Fig. 28 which is a modification of the block S26 in Figs. 5 and 6. With reference to Fig. 28, a first step S31 in the block S26A decides whether or not the signal type flag corresponds to an audio signal (a DVD-Audio audio pack). When the signal type flag corresponds to an  
25 audio signal, the program advances from the step S31 to a step S32A. Otherwise, the program advances from the step S31 to a step

S33.

The step S32A decides whether or not an active DSD flag (or a DSD head identifier) is present. When the active DSD flag is present, the program advances from the step S32A to a step S32B.

- 5 Otherwise, the program advances from the step S32A to a step S32C.

The step S32B feeds a received signal (the received main data) to a buffer assigned to DSD audio data and provided in the reception side. After the step S32B, the program jumps to the step  
10 S27 (see Fig. 5).

The step S32C feeds a received signal (the received main data) to a buffer assigned to PCM audio data and provided in the reception side. After the step S32C, the program jumps to the step S27 (see Fig. 5).

- 15 The eighth embodiment of this invention includes a disc player of Fig. 29 which is a modification of the disc player in Fig. 21. The disc player of Fig. 29 includes an audio signal processor 9A instead of the audio signal processor 9 (see Fig. 21).

With reference to Fig. 29, the audio signal processor 9A is  
20 controlled by a control unit 14 in response to a DSD flag (or a DSD head identifier). In the case where a DSD flag is active, the audio signal processor 9A subjects an audio data stream to a DSD conversion process and thereby converts the audio data stream into a DSD audio signal. The audio signal processor 9A outputs the DSD  
25 audio signal to a data arranger 12 and a D/A converter 17. In the case where the DSD flag is inactive, the audio signal processor 9A

subjects an audio data stream to a PCM conversion process and thereby converts the audio data stream into a PCM audio signal. The audio signal processor 9A outputs the PCM audio signal to the data arranger 12 and the D/A converter 17.

5           The D/A converter 17 changes the DSD audio signal or the PCM audio signal into a corresponding analog audio signal. The analog audio signal can be transmitted from the D/A converter 17 to an external loudspeaker. In this case, the analog audio signal is converted into corresponding sounds by the external loudspeaker.

10           The data arranger 12 subjects the output signal of the audio signal processor 9A to a data arranging process, thereby converting the output signal of the audio signal processor 9A into an arrangement-resultant digital audio signal. The data arranger 12 outputs the arrangement-resultant digital audio signal to data  
15           transmission interfaces 13-1 and 13-2.

          The eighth embodiment of this invention includes a recording and reproducing device of Fig. 30 which is a modification of the recording and reproducing device in Fig. 22. The recording and reproducing device of Fig. 30 includes a PCM-DSD converter 28a  
20           connected between an audio pack processor 23 and a terminal 28. The recording and reproducing device of Fig. 30 includes buffers 23V-1 and 23V-2 instead of the buffer 23V (see Fig. 22). The buffers 23V-1 and 23V-2 are connected to the audio pack processor 23. The buffer 23V-1 is assigned to DSD audio data. The buffer  
25           23V-2 is assigned to PCM audio data.

          With reference to Fig. 30, a control unit 32 operates to

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The step S260 expands and decodes lossless-compression-resultant audio data to recover original audio data. After the step S260, the program advances to the step S270.

5 The step S270 feeds the audio data generated by the step S260 or the audio data unprocessed by the step S260 to a D/A converter (not shown). After the step S270, the program returns to the step S250.

#### Other Embodiments

10 Each of the first, second, third, fourth, seventh, and eighth embodiments of this invention may be modified to include only one IEEE1394 serial interface for two-way communications.

In each of the first, second, third, fourth, fifth, seventh, and eighth embodiments of this invention, the disc player may be replaced by an HDD (hard disc drive) device.

15 In each of the first, second, third, fourth, sixth, seventh, and eighth embodiments of this invention, the recording and reproducing device may be replaced by a portable terminal device or a mobile terminal device.